

Preparation of abstracts

Abstracts should be noncommercial in scope, pertinent to the workshop topic, and no longer than 500 words. They should include:

- ◆ Title of presentation
- ◆ Author(s)
- ◆ Affiliation(s)
- ◆ Authors' complete postal and e-mail address(es)
- ◆ Telephone and fax numbers
- ◆ Short synopsis of the paper

Please note: Acceptance of a paper does not waive the symposium registration fee; however, special registration fees will be available to presenters.

Submission of abstracts

The preferred method of abstract submission is in Microsoft Word, DOS, or ASCII text format e-mailed to <itea-hpc-workshop@arl.mil>. Those without e-mail access may fax or mail submissions to:

Director, U.S. Army Research Laboratory (ARL)
Attn: AMSRL-CI-HC, John Grosh
Aberdeen Proving Ground, MD 21005-5067
Fax: 410-278-9199

Please direct questions regarding abstract submissions to Gindy Feeser, 410-278-7195, <gfeeser@arl.mil> or John Grosh, 410-278-6705, <jgrosh@arl.mil>.

The deadline for the submission of abstracts is **March 20, 1998**. Authors will be notified of their selection by **April 30, 1998**. Papers must be received by **May 30, 1998** for inclusion in the proceedings.

Additional information

Web Site	http://www.arl.hpc.mil/itea	
	HPC	T&E
Workshop Chairs	Dr. Charlie Holland 703-812-8205	Richard Pace 703-697-4819
Workshop Coordinators	Dr. Danny Weddle 301-342-3131	Dr. C. David Brown 410-278-1474
Technical Program - Chairs	Dr. Andy Mark 410-278-9761	Dr. Donald Haskell 410-306-0223
Technical Program - Papers	John Grosh 410-278-6705	Rick Cozby 410-278-1481
Workshop Facilitators	Regis Sullivan 703-812-8205	Rudy Ayala 410-278-1269
Short Courses	Virginia To 410-278-9282	

Exhibits

Milt Strickland, (505) 821-3591, mh-strickland@itea.org

Registration

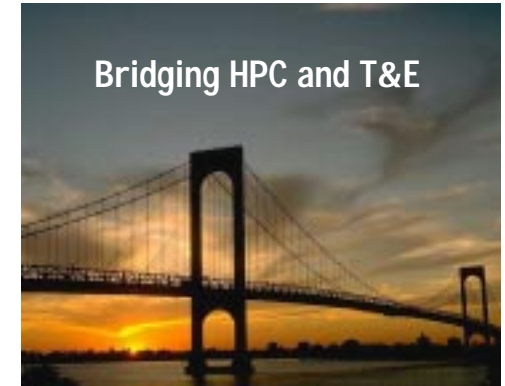
International Test and Evaluation Association
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High Performance Computing in Support of Test and Evaluation

July 13 - 16, 1998

Sheraton Four Points Hotel
980 Hospitality Way
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International Test and Evaluation
Association Workshop

Workshop sponsored by the ITEA Francis Scott Key Chapter and hosted by Headquarters, U.S. Army Test and Evaluation Command, the Department of Defense High Performance Computing Modernization Office and the Army Research Laboratory High Performance Computing Major Shared Resource Center

High Performance Computing in Support of Test and Evaluation

July 13 – 16, 1998 ♦ Sheraton Four Points Hotel ♦ Aberdeen, Maryland

Program highlights

- ♦ Short courses
- ♦ Blue ribbon panels
- ♦ Three technical sessions (ten papers each)
- ♦ Keynote and featured speakers
- ♦ Three evening activities
 - ✓ Exhibits, live computer demonstrations, and poster papers
 - ✓ Waterfront reception & cruise
 - ✓ Winetasting & tour
- ♦ Guest tour
- ♦ Three tours
 - ✓ Army Research Lab (ARL)
 - ✓ ARL Major Shared Resource Center
 - ✓ Aberdeen Test Center
- ♦ Francis Scott Key Chapter luncheon

Featured speakers

- ♦ *Senator Barbara A. Mikulski* (D-Md.) [invited]
- ♦ *The Hon. Philip E. Coyle III*, Director, Operational Test and Evaluation
- ♦ *Dr. Patricia Sanders*, Director, Test, Systems Engineering and Evaluation, Office of the Under Secretary of Defense (Acquisition and Technology)
- ♦ *Dr. Charles J. Holland*, Director, DoD High Performance Computing Modernization Program Office
- ♦ *John F. Gehrig*, Deputy Director, Test, Systems Engineering and Evaluation/ Resources and Ranges
- ♦ *George Singley*, Acting Director, Defense Research and Engineering [invited]
- ♦ *Dr. Larry Smarr*, Director, National Center for Supercomputing Applications

Focus

This workshop will provide a forum for representatives from government, industry, and academia to hear and present papers on policy and technical issues, view exhibits and poster papers, and establish networks to foster the bridging of the Defense Department's High Performance Computing (HPC) and Test and Evaluation (T&E) communities. The goals of the HPC program are "to enable the United States to maintain its technological supremacy over its adversaries in weapon systems design and to foster the flow of this technology into warfighting support systems." The T&E community supports the warfighter by ensuring that developing systems are technically and operationally sound. The HPC program will enhance that support by providing a wide range of resources, which will include hardware, software, data storage, archiving, visualization, training, and expertise in specific computational technology areas.

Fees

	<i>prior to June 17</i>	<i>June 17– July 1</i>	<i>After July 1</i>
ITEA Members	\$310	\$335	\$385
Nonmembers	\$360	\$385	\$435
Full-time students	\$10	\$10	\$10

Call for papers

Technical papers discussing the potential synergy between the HPC and T&E communities are invited, particularly in the following areas:

- ♦ SIMULATION, TEST AND EVALUATION PROCESS and SIMULATION-BASED ACQUISITION
- ♦ INFORMATION MANAGEMENT
 - ✓ Data acquisition and data processing
 - ✓ HPC accomplishments
 - ✓ HPC plans and issues
 - ✓ T&E successes with HPC
 - ✓ T&E plans and requirements
 - ✓ Major range requirements
- ♦ NETWORKING AND DISTRIBUTED OPERATIONS
 - ✓ Scalable parallel computing
 - ✓ Distributed computing
 - ✓ HPC accomplishments
 - ✓ HPC plans and issues
 - ✓ T&E successes with HPC
 - ✓ T&E plans and requirements
 - ✓ Interactive, real-time operations

All sessions will be at the Sheraton Four Points Hotel and will be unclassified. Abstracts will be reviewed for presentation during a workshop session or as a poster paper. Papers selected for presentation will be published in proceedings on the World Wide Web and on CD-ROM media.